

VAP on
High Performance Computing for AI

1st to 5th September 2025 | 8.45AM to 4.35PM

CSE Block 3rd Floor, Acharya Campus

About the Program

This program is designed to equip participants with the knowledge and skills needed to leverage High Performance Computing (HPC) for advanced Artificial Intelligence (AI) applications. Through a blend of theoretical concepts and hands-on practical sessions, learners will explore HPC architectures, parallel programming techniques, and AI frameworks optimized for high-speed computation. The course aims to enable participants to accelerate AI model training, handle large datasets efficiently, and develop scalable AI solutions. Ideal for students, researchers, and professionals, this program prepares participants to meet the growing computational demands of cutting-edge AI research and industrial applications.

Objectives of the Program

- To develop a strong understanding of HPC architectures and their role in accelerating AI computations.
- To equip participants with practical skills in implementing and optimizing AI algorithms using HPC technologies.

Expected Outcomes of the Program






- Participants will demonstrate the ability to efficiently utilize HPC systems to accelerate AI model training and inference.
- Learners will gain hands-on experience with HPC tools, frameworks, and parallel programming techniques tailored for AI applications.
- Graduates will be capable of designing and deploying scalable AI solutions that effectively handle large datasets using HPC infrastructure.
- Participants will develop problem-solving skills to optimize AI workloads and improve computational performance on HPC platforms.
- The program will enhance participants' readiness to contribute to cutting-edge AI research and industrial projects requiring high computational power.

Target Audience: Final year CSE Students

Committee Members

| ADVISORY COMMITTEE |
|---|
| <ul style="list-style-type: none">• Dr. Marigowda C K, Principal, AIT• Dr. Rajeswari, Dean Academics, AIT |
| CONVENER |
| <ul style="list-style-type: none">• Dr. Kala Venugopal, Head of the Department, Dept. of CS&E, AIT |
| EVENT COORDINATORS |
| <ul style="list-style-type: none">• Prof. Abhishek B M, Assistant Professor, Dept. of CS&E, AIT. Email: abhishek3088@acharya.ac.in, Mobile: +91 97415 36310• Prof. Bharathi K, Assistant Professor, Dept. of CS&E, AIT. Email: bharathi2680@acharya.ac.in, Mobile: +91 99456 08827 |

Resource Person Details

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|---|---|---|--|
|  | <p>Mr. Ayush Dixit M.Tech Designation: VLSI from IIT Jodhpur, Working in Sandlogic Technologies as an RTL Design Engineer</p> |  | <p>Dr. Bhargav N Designation: Senior Education Specialist, Tata Technologies Limited</p> |
|  | <p>Dr. Ajith Padyana Designation: Professor, Dept. of CS&E, AIT</p> |  | <p>Prof. Vishal Santhwane Designation: Assistant Professor, Dept. of CS&E, AIT</p> |
|  | <p>Prof. Rakshitha BT Designation: Professor, Dept. of CS&E, AIT</p> | | |



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Event Coordinator

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