ACHARYA POLYTECHNIC

WORKSHOP ON PCB DESIGNING BY TECHIES DESIGN

29th & 30th July 2025 | 10:00 am Onwards

Electronics and Communication Lab, AP, Acharya Campus

About the Program

This hands-on workshop on Printed Circuit Board (PCB) Designing, conducted by Techies Design, aims to equip participants with practical skills and foundational knowledge in electronic circuit design and PCB layout development.

Through a combination of expert-led instruction and interactive sessions, participants gain experience using industry-standard software tools to design, simulate, and prototype PCBs. The workshop covers essential topics such as circuit schematics, component placement, routing techniques, and fabrication guidelines. Designed for diploma students and electronics enthusiasts, this program bridges academic learning with real-world applications, fostering innovation and technical confidence in hardware design.

Objectives of the Program

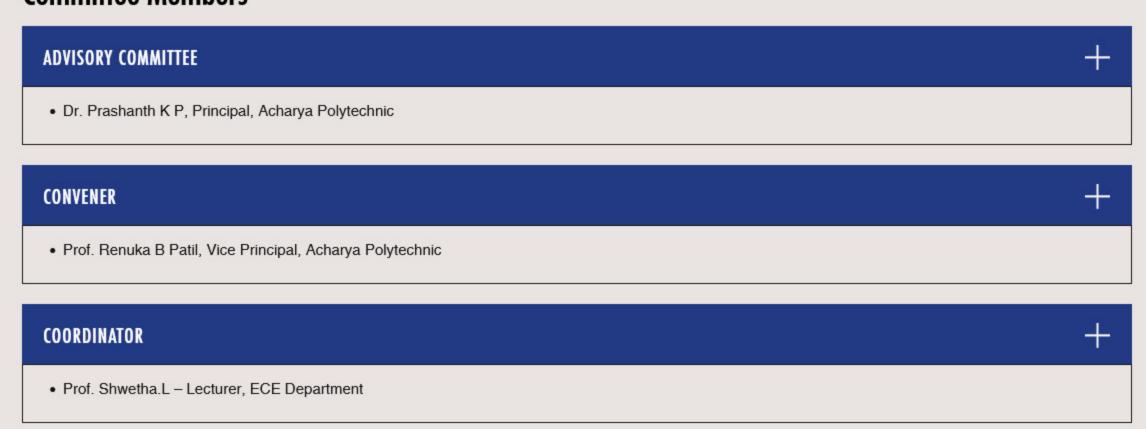- To introduce the fundamentals of PCB design and its role in electronic circuit development.
- To train participants in using PCB design software such as Eagle, KiCad, or Altium.
- To provide hands-on experience in designing, simulating, and generating PCB layouts.
- To prepare students for real-time electronics projects and industrial design practices.

Expected Outcomes of the Program

- Participants gain a clear understanding of PCB design concepts, components, and layout techniques.
- Ability to use industry-standard PCB design tools confidently for creating and editing circuits.
- Creation of a basic working PCB design as a practical takeaway from the workshop.
- Improved readiness for academic projects, internships, and careers in electronics and embedded systems.

Target Audience: II and III year students of ECE Department

Committee Members

ADVISORY COMMITTEE	+
• Dr. Prashanth K P, Principal, Acharya Polytechnic	

CONVENER	+
• Prof. Renuka B Patil, Vice Principal, Acharya Polytechnic	

COORDINATOR	+
• Prof. Shwetha.L – Lecturer, ECE Department	

Resource Person Details

Mr. Murugendra Kotigiri

Designation : Managing Director, Techies Designs Electronics Pvt. Ltd.

Mr. Pavan A

Designation : Hardware Design Engineer, Techies Designs Electronics Pvt. Ltd.

Acharya Polytechnic

Acharya Dr. S. Radhakrishnan Road, Acharya P.O Soladevanahalli, Bangalore - 560107, Karnataka, India.

LOCATE US

Event Coordinator

Prof. Shwetha.L

Academic Coordinator, Acharya Polytechni